

**Amendments to the Specification:**

Please replace the paragraph beginning at page 51, line 12, with the following amended paragraph:

Fig. 25B is a sectional view showing the structure of the EL display device in this Example. There is shown a substrate 4010, an underlying coating 4021, a TFT 4022 for the driving circuit 4024, and a TFT 4023 for the pixel unit 4025. (The TFT 4022 shown is a CMOS circuit consisting of an n-channel type TFT and a p-channel type TFT. The TFT 4023 shown is the one, which controls current to the EL element.) These TFTs may be of any known structure (top gate structure or bottom gate structure).